

FIG. 1

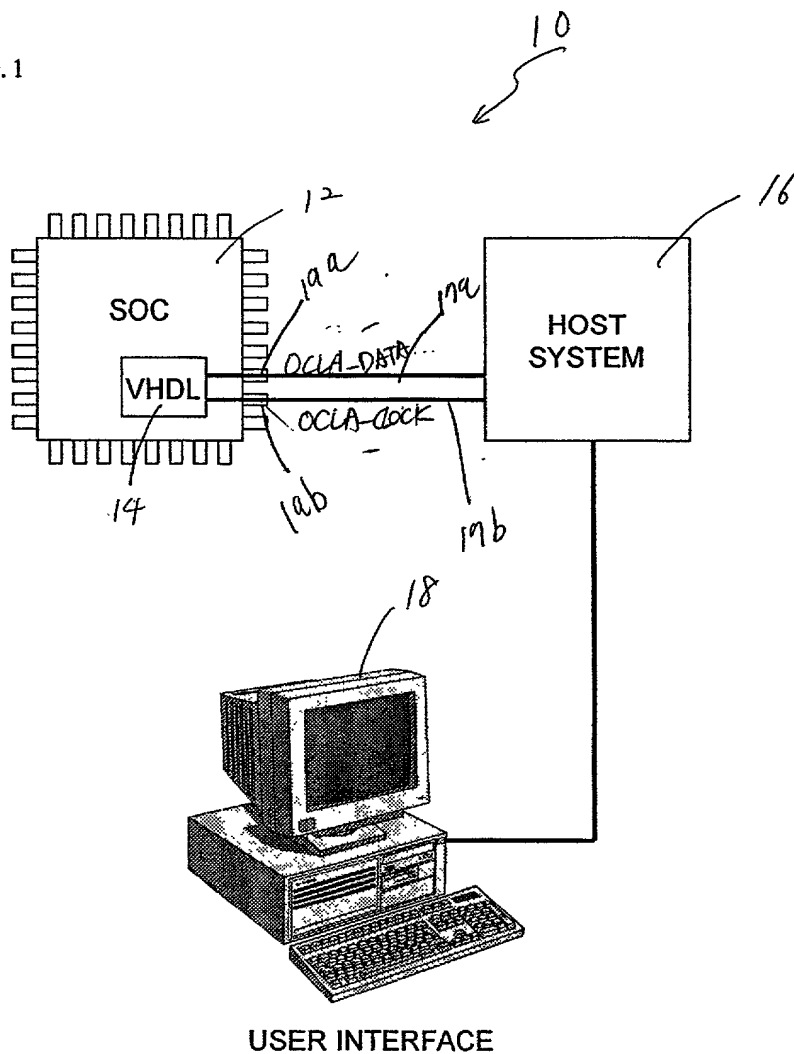


FIG. 2

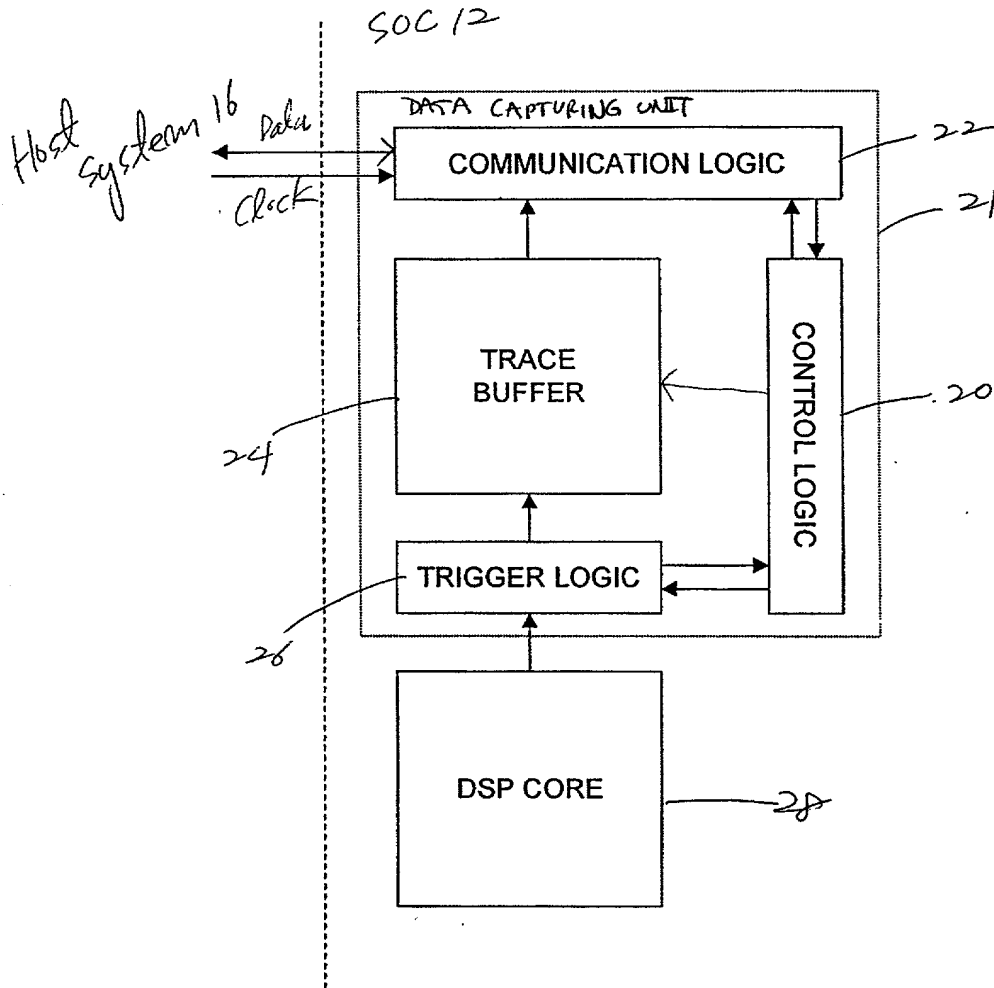


FIG. 3

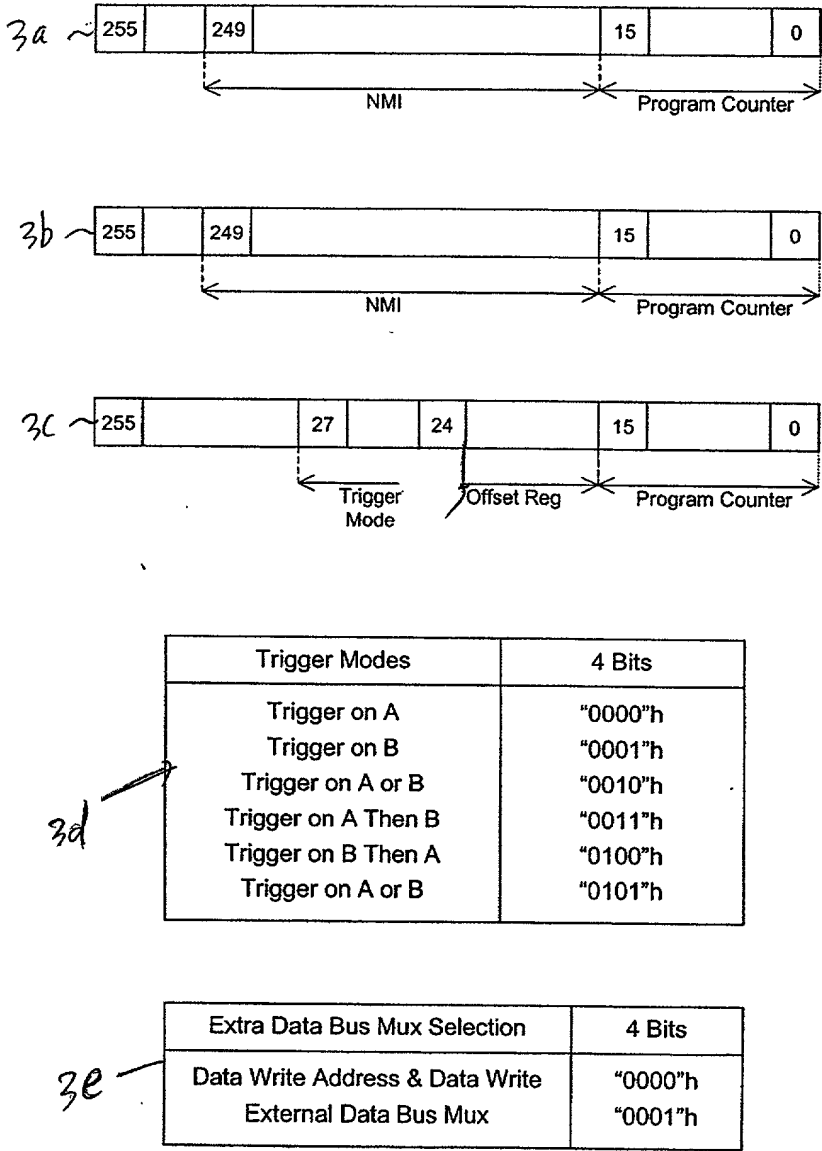


FIG. 4A

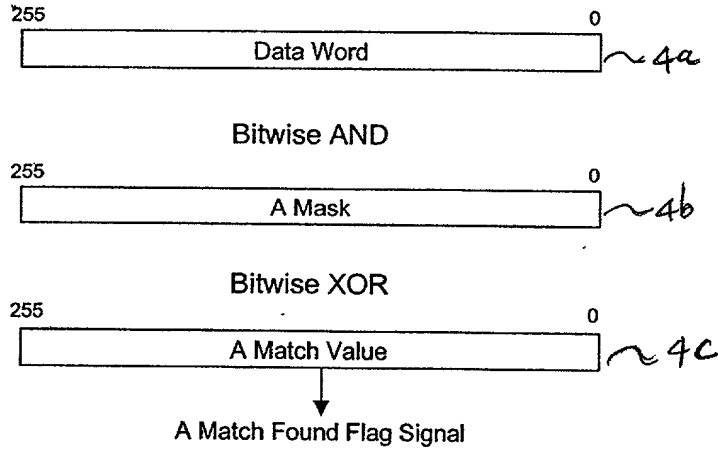


FIG. 4B

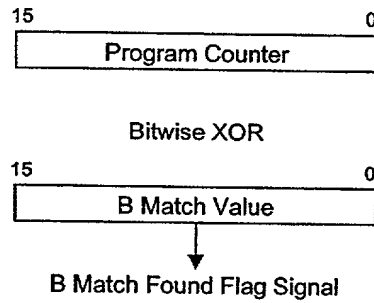


FIG. 5A

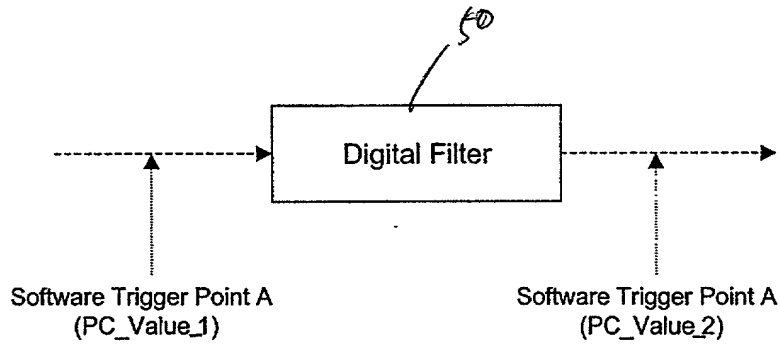


FIG. 5B

PC	Label	Label
1000	PC_Value_1	LOAD input into a DSP Register
1002	1002	LOAD starting point of addressing registers
1004	1004	LOAD repeat loop register with N
1006	1006	CLEAR multiply accumulate register
1008	1008	MULTIPLY-ACCUMULATE N times, update pointers
100A	100A	SHIFT to normalize result
100C	PC_Value_2	STORE result

FIG. 6

